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C. Moore

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

**SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT**

Docket Number  
**10191/2197**

Application Number  
**10/030,309**

Filing Date  
**May 13, 2002**

Examiner  
**Dana Farahani**

Art Unit  
**2814**

Title

**DIODE HAVING A METAL SEMICONDUCTOR  
CONTACT, AND METHOD FOR THE  
MANUFACTURE THEREOF**

Applicant(s)

**Alfred GOERLACH**

Address to:  
Commissioner for Patents  
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on

Date:

*Apr. 14, 2003*

Reg. No. 22,490

Signature:

*Richard L. Mayer*

Richard L. Mayer

*Reg. No. 42,194*

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant(s) hereby bring the following reference(s) to the attention of the Examiner. The reference(s) are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.
3. It is believed that no fees are due in connection with this Supplemental Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account No. 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated: *April 4, 2003*

By:

*Richard L. Mayer*

Richard L. Mayer (Reg. No. 22,490)

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**SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT BY  
APPLICANT - PTO FORM 1449**

Atty. Docket No.

**10191/2197**

Serial No.

**10/030,309**

Applicant(s)

**Alfred GOERLACH**

Filing Date

May 13, 2002

Group

2814

**U. S. PATENT DOCUMENTS**

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO
	197 05 728	August 20, 1998	DE				
	08264811	October 11, 1996	JP				

**OTHER DOCUMENTS**

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	
	K. J. Schoen et al., "High Voltage GaInP/GaAs Dual Material Schottky Rectifiers," Appl. Phys. Lett. Vol. 71, No. 4, pp. 518 to 520, July 28, 1997	

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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